

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	1070	tuttle.in.	USPAT	OR	ON	2007/02/02 17:22
S2	79	tuttle.in. and transceiv\$6	USPAT	OR	ON	2007/08/28 15:22
S3	3	("6325294" "6220516" "5776278").pn.	USPAT	OR	ON	2005/10/03 12:09
S4	1	("6325294").pn. and backing	USPAT	OR	ON	2005/10/03 12:09
S5	3319	(card smart\$1card ((storage memor\$5) adj2 device)) with (coupl\$4 join\$6 connect\$5 contact\$5) with (first upper) with (second lower) with (source batter\$4 power\$5)	USPAT	OR	ON	2007/02/02 17:30
S6	189	235/492.ccls. and ((coupl\$4 join\$6 connect\$5 contact\$5) with (first upper) with (second lower) with (source batter\$4 power\$5))	USPAT	OR	ON	2007/02/02 17:33
S7	354	(235/492.ccls. 340/572.\$.ccls.) and ((coupl\$4 join\$6 connect\$5 contact\$5) with (first upper) with (second lower) with (source batter\$4 power\$5))	USPAT	OR	ON	2007/02/02 17:35
S8	380	(235/487,492.ccls. 340/572.\$.ccls.) and ((coupl\$4 join\$6 connect\$5 contact\$5) with (first upper) with (second lower) with (source batter\$4 power\$5))	USPAT	OR	ON	2007/02/02 17:47
S9	715	(235/487,492.ccls. 340/572.\$.ccls.) and ((coupl\$4 join\$6 connect\$5 contact\$5) with (((first upper) with (second lower)) two dual\$4) with (source batter\$4 power\$5 voltage))	USPAT	OR	ON	2007/02/02 17:48
S10	283	(235/487,492.ccls. 340/572.\$.ccls.) and ((coupl\$4 join\$6 connect\$5 contact\$5) with (((first upper) with (second lower)) two dual\$4) with (source batter\$4 power\$5 voltage)) and ((stor\$6 memor\$6 record\$4) near4 (source batter\$4 power\$4 voltage\$4))	USPAT	OR	ON	2007/08/28 15:14

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S11	375	(235/487,492.ccls. 340/572.\$.ccls.) and ((coupl\$4 join\$6 connect\$5 contact\$5) with (((first upper) with (second lower)) two dual\$4) with (source batter\$4 power\$5 voltage)) and ((stor\$6 memor\$6 record\$4 ic (integrated adj2 circuit)) near4 (source batter\$4 power\$4 voltage\$4))	USPAT	OR	ON	2007/08/28 15:17
S12	337	(235/487,492.ccls. 340/572.\$.ccls.) and ((coupl\$4 join\$6 connect\$5 contact\$5) with (((first upper) with (second lower)) two dual\$4) with (source batter\$4 power\$5 voltage)) and ((stor\$6 memor\$6 record\$4 ic) near4 (source batter\$4 power\$4 voltage\$4))	USPAT	OR	ON	2007/02/03 13:11
S13	389	(235/487,492.ccls. 340/572.\$.ccls.) and ((coupl\$4 join\$6 connect\$5 contact\$5) with (((first upper) with (second lower)) two dual\$4) with (source batter\$4 power\$5 voltage energ\$6)) and ((stor\$6 memor\$6 record\$4 ic) near4 (source batter\$4 power\$4 voltage\$4 energ\$6))	USPAT	OR	ON	2007/02/03 13:17
S14	52	S13 not S12	USPAT	OR	ON	2007/02/03 13:12
S15	1119	(235/487,492.ccls. 340/572.\$.ccls.) and ((fold\$6 switch\$6) with (source batter\$4 power\$5 voltage energ\$6))	USPAT	OR	ON	2007/02/03 15:12
S19	410	(235/487,492.ccls. 340/572.\$.ccls.) and ((fold\$6 switch\$6) with (source batter\$4 power\$5 voltage energ\$6) with (cpu micro\$1process\$4. micro\$1control\$5 chip module ram rom eeprom eprom stor\$6 memor\$6 record\$4 ic (integrat\$4 adj circuit)))	USPAT	OR	ON	2007/02/03 13:36
S20	1	"5212664".PN.	USPAT; USOCR	OR	OFF	2007/02/03 13:52
S21	1	"5343436".PN.	USPAT; USOCR	OR	OFF	2007/02/03 13:52
S23	65	(235/487,492.ccls. 340/572.\$.ccls.) and (static with random with access with memory)	USPAT	OR	ON	2007/02/03 15:51
S24	27	rovin.in.	USPAT	OR	ON	2007/02/03 15:51

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S25	298	(235/487,492.ccls. 340/572\$.ccls.) and ((move\$6 moving shift\$5 pivot\$5 hing\$5 remov\$6 detach\$5 attacgh\$5) with (source batter\$4 power\$5 voltage energ\$6) with (rom ram eprom eeprom ic (integrat\$5 adj2 circuit\$4) module cpu micro\$1process\$5 micro\$1control\$5 memor\$6 stor\$6 record\$6))	USPAT	OR	ON	2007/08/28 15:19
S26	0	(235/487,492.ccls. 340/572\$.ccls.) and (micro\$1sprocess\$6 and (spread with spectrum) and ((transmi\$6 with receiv\$6) transceiv\$7))	USPAT	OR	ON	2007/02/03 15:57
S27	79	(235/487,492.ccls. 340/572\$.ccls.) and (micro\$1process\$6 and (spread with spectrum) and ((transmi\$6 with receiv\$6) transceiv\$7))	USPAT	OR	ON	2007/02/03 15:58
S28	127	(235/487,492.ccls. 340/572\$.ccls.) and ((spread with spectrum) and ((transmi\$6 with receiv\$6) transceiv\$7))	USPAT	OR	ON	2007/02/05 09:14
S29	21	(235/487,492.ccls. 340/572\$.ccls.) and (hous\$4 with thick\$4 with inches)	USPAT	OR	ON	2007/02/03 16:01
S30	30	(235/487,492.ccls. 340/572\$.ccls.) and ((hous\$4 case casing) with (thick\$4 tall\$4) with inches)	USPAT	OR	ON	2007/02/03 16:05
S31	129	(235/487,492.ccls. 340/572\$.ccls.) and (conduct\$6 with epoxy)	USPAT	OR	ON	2007/02/03 16:05
S32	57	(235/487,492.ccls. 340/572\$.ccls.) and (conduct\$6 with epoxy with (source batter\$4 power\$5 voltage energ\$6 rom ram eprom eeprom ic (integrat\$5 adj2 circuit\$4) module cpu micro\$1process\$5 micro\$1control\$5 memor\$6 stor\$6 record\$6))	USPAT	OR	ON	2007/02/03 16:07
S33	129	(235/487,492.ccls. 340/572\$.ccls.) and (spread with spectrum)	USPAT	OR	ON	2007/02/05 10:05
S34	173	(integrat\$4 adj2 circuit\$4) with (spread with spectrum)	USPAT	OR	ON	2007/02/05 09:26
S35	1692	((integrat\$4 adj2 circuit\$4) ic chip) with (spread with spectrum)	USPAT	OR	ON	2007/02/05 09:26

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S36	1216	((integrat\$4 adj2 circuit\$4) ic chip) with (spread with spectrum)) and ((rf radio) with (transmi\$7 receiv\$7 transceiv\$6))	USPAT	OR	ON	2007/02/05 09:27
S37	41	((integrat\$4 adj2 circuit\$4) ic chip) with (spread with spectrum)) and ((rf radio) with (transmi\$7 receiv\$7 transceiv\$6)) and ((card smart\$1card) with ((integrat\$4 adj2 circuit\$4) ic chip))	USPAT	OR	ON	2007/02/05 09:28
S38	63	((integrat\$4 adj2 circuit\$4) ic chip) with (spread with spectrum)) and ((rf radio) with (transmi\$7 receiv\$7 transceiv\$6)) and ((card smart\$1card ((storag\$4 memor\$6) adj device))) with ((integrat\$4 adj2 circuit\$4) ic chip))	USPAT	OR	ON	2007/02/05 09:31
S39	1000	(235/487,492.ccls. 340/572.\$ccls.) and ((rf radio) with ((transmi\$7 with receiv\$6) transceiv\$5))	USPAT	OR	ON	2007/02/05 10:06
S40	78	(235/487,492.ccls. 340/572.\$ccls.) and ((micro\$1process\$6 micro\$1control\$6) with (rf radio) with ((transmi\$7 with receiv\$6) transceiv\$5))	USPAT	OR	ON	2007/02/07 09:34
S41	2	(235/487,492.ccls. 340/572.\$ccls.) and ((micro\$1process\$6 micro\$1control\$6) with (micro\$1wave) with ((transmi\$7 with receiv\$6) transceiv\$5))	USPAT	OR	ON	2007/02/07 09:34
S42	1	"4686358".pn.	USPAT	OR	OFF	2007/08/28 15:04
S43	304	(235/487,492.ccls. 340/572.\$ccls.) and ((coupl\$4 join\$6 connect\$5 contact\$5) with (((first upper) with (second lower)) two dual\$4) with (source batter\$4 power\$5 voltage)) and ((stor\$6 memor\$6 record\$4) near4 (source batter\$4 power\$4 voltage\$4))	USPAT	OR	ON	2007/08/28 15:15
S44	400	(235/487,492.ccls. 340/572.\$ccls.) and ((coupl\$4 join\$6 connect\$5 contact\$5) with (((first upper) with (second lower)) two dual\$4) with (source batter\$4 power\$5 voltage)) and ((stor\$6 memor\$6 record\$4 ic (integrated adj2 circuit)) near4 (source batter\$4 power\$4 voltage\$4))	USPAT	OR	ON	2007/08/28 15:17

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S45	425	(235/487,492.ccls. 340/572.\$.ccls.) and ((move\$6 moving shift\$5 pivot\$5 hing\$5 remov\$6 detach\$5 attach\$5) with (source batter\$4 power\$5 voltage energ\$6) with (rom ram eprom eeprom ic (integrat\$5 adj2 circuit\$4) module cpu micro\$1process\$5 micro\$1control\$5 memor\$6 stor\$6 record\$6))	USPAT	OR	ON	2008/01/04 19:00
S46	425	tuttle.in. and (first with second)	USPAT	OR	ON	2007/08/28 15:22
S47	91	tuttle.in. and (first with second) and transceiv\$6	USPAT	OR	ON	2007/08/28 15:35
S48	41	tuttle.in. and (first with second) and transceiv\$6	US-PGPUB	OR	ON	2007/08/28 15:35
S49	438	(235/487,492.ccls. 340/572.\$.ccls.) and ((move\$6 moving shift\$5 pivot\$5 hing\$5 remov\$6 detach\$5 attach\$5) with (source batter\$4 power\$5 voltage energ\$6) with (rom ram eprom eeprom ic (integrat\$5 adj2 circuit\$4) module cpu micro\$1process\$5 micro\$1control\$5 memor\$6 stor\$6 record\$6))	USPAT	OR	ON	2008/01/04 19:00